

Intel[®] Data Plane Development Kit -Link Status Interrupt Sample Application

User Guide

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Revision History

Date	Revision	Description
April 2012	1.0	Initial release



1.0 Introduction

The Link Status Interrupt sample application is a simple example of packet processing using the Intel[®] Data Plane Development Kit (Intel[®] DPDK) that demonstrates how network link status changes for a network port can be captured and used by an Intel[®] DPDK application.

1.1 Documentation Roadmap

The following is a list of Intel[®] DPDK documents in suggested reading order:

- **Release Notes**: Provides release-specific information, including supported features, limitations, fixed issues, known issues and so on. Also, provides the answers to frequently asked questions in FAQ format.
- **Getting Started Guide**: Describes how to install and configure the Intel[®] DPDK software; designed to get users up and running quickly with the software.
- Programmer's Guide: Describes:
 - The software architecture and how to use it (through examples), specifically in a Linux* application (linuxapp) environment
 - The content of the Intel[®] DPDK, the build system (including the commands that can be used in the root Intel[®] DPDK Makefile to build the development kit and an application) and guidelines for porting an application
 - Optimizations used in the software and those that should be considered for new development

A glossary of terms is also provided.

- **API Reference**: Provides detailed information about Intel[®] DPDK functions, data structures and other programming constructs.
- **Sample Application User Guides**: A set of guides, each describing a sample application that showcases specific functionality, together with instructions on how to compile, run and use the sample application.

2.0 Overview

The Link Status Interrupt sample application registers a user space callback for the link status interrupt of each port and performs L2 forwarding for each packet that is received on an RX_PORT. The following operations are performed:

- RX_PORT and TX_PORT are paired with available ports one-by-one according to the core mask
- The source MAC address is replaced by the TX_PORT MAC address
- The destination MAC address is replaced by 00:09:c0:00:00:TX_PORT_ID

This application can be used to demonstrate the usage of link status interrupt and its user space callbacks and the behavior of L2 forwarding each time the link status changes.

3.0 Compiling the Application

1. Go to the example directory:

```
export RTE_SDK=/path/to/rte_sdk
cd ${RTE_SDK}/examples/link_status_interrupt
```



2. Set the target (a default target is used if not specified). For example:

export RTE_TARGET=x86_64-default-linuxapp-gcc

See the Intel[®] DPDK Getting Started Guide for possible RTE_TARGET values.

3. Build the application:

make

Note: The compiled application is written to the build subdirectory. To have the application written to a different location, the O=/path/to/build/directory option may be specified on the make command line.

4.0 **Running the Application**

The application requires a number of command line options:

```
./build/link_status_interrupt [EAL options] -- -p PORTMASK [-q NQ]
```

where,

- -p PORTMASK: A hexadecimal bitmask of the ports to configure
- -q NQ: A number of queues (=ports) per lcore (default is 1)

To run the application in a linuxapp environment with 4 lcores, 4 memory channels, 16 ports and 8 RX queues per lcore, issue the command:

\$./build/link_status_interrupt -c f -n 4-- -q 8 -p ffff

Refer to the *Intel[®] DPDK Getting Started Guide* for general information on running applications and the Environment Abstraction Layer (EAL) options.

5.0 Explanation

The following sections provide some explanation of the code.

5.1 Command Line Arguments

The Link Status Interrupt sample application takes specific parameters, in addition to Environment Abstraction Layer (EAL) arguments (see Section 4.0).

Command line parsing is done in the same way as it is done in the L2 Forwarding Sample Application. See Section 5.1 in the $Intel^{®}$ Data Plane Development Kit - *L2* Forwarding Sample Application User Guide for more information.

5.2 Mbuf Pool Initialization

Mbuf pool initialization is done in the same way as it is done in the L2 Forwarding Sample Application. See Section 5.2 in the $Intel^{\ensuremath{\mathbb{R}}}$ Data Plane Development Kit - *L2 Forwarding Sample Application User Guide* for more information.



5.3 Driver Initialization

The main part of the code in the main() function relates to the initialization of the driver. To fully understand this code, it is recommended to study the chapters that related to the Poll Mode Driver in the $Intel^{\textcircled{R}}$ DPDK Programmer's Guide and the $Intel^{\textcircled{R}}$ DPDK API Reference.

```
/* init driver(s) */
#ifdef RTE_LIBRTE_IGB_PMD
    if (rte igb pmd init() < 0)
        rte_exit(EXIT_FAILURE, "Cannot init igb pmd\n");
#endif
#ifdef RTE LIBRTE_IXGBE_PMD
    if (rte_ixgbe_pmd_init() < 0)</pre>
        rte exit(EXIT FAILURE, "Cannot init ixgbe pmd\n");
#endif
    if (rte eal pci probe() < 0)</pre>
        rte exit(EXIT FAILURE, "Cannot probe PCI\n");
    nb_ports = rte_eth_dev_count();
    if (nb_ports == 0)
        rte_exit(EXIT_FAILURE, "No Ethernet ports - bye\n");
    if (nb ports > L2FWD MAX PORTS)
        nb_ports = L2FWD_MAX_PORTS;
    nb_lcores = rte_lcore_count();
    /* initialize all ports */
    for (portid = 0; portid < nb_ports; portid++) {</pre>
        /* ... */
        /* rx and tx queue init, refer to the source code for details */
        /* ... */
    }
```

Observe that:

- rte_igb_pmd_init() or rte_ixgbe_pmd_init() simultaneously registers the driver as a PCI driver and as an Ethernet* poll mode driver.
- rte_eal_pci_probe() parses the devices on the PCI bus and initializes recognized devices.

The next step is to configure the RX and TX queues. For each port, there is only one RX queue (only one lcore is able to poll a given port). The number of TX queues depends on the number of available lcores. The rte_eth_dev_configure() function is used to configure the number of queues for a port:

The global configuration is stored in a static structure:

```
static const struct rte_eth_conf port_conf = {
    .rxmode = {
        .split_hdr_size = 0,
        .header_split = 0, /**< Header Split disabled */
        .hw_ip_checksum = 0, /**< IP checksum offload disabled */
        .hw_vlan_filter = 0, /**< VLAN filtering disabled */
        .jumbo_frame = 0, /**< Jumbo Frame Support disabled */
    },
    .txmode = {
    },</pre>
```



```
.intr_conf = {
    .lsc = 1, /**< link status interrupt feature enabled */
    },
};</pre>
```

Configuring lsc to 0 (the default) disables the generation of any link status change interrupts in kernel space and no user space interrupt event is received. The public interface rte_eth_link_get() accesses the NIC registers directly to update the link status. Configuring lsc to non-zero enables the generation of link status change interrupts in kernel space when a link status change is present and calls the user space callbacks registered by the application. The public interface rte_eth_link_get() just reads the link status in a global structure that would be updated in the interrupt host thread only.

5.4 Interrupt Callback Registration

The application can register one or more callbacks to a specific port and interrupt event. An example callback function that has been written as indicated below.

```
static void
lsi event callback(uint8 t port id, enum rte eth event type type, void *param)
    struct rte_eth_link link;
        /* avoid compile warning */
        param = param;
       printf("\n\nIn registered callback...\n");
       printf("Event type: %s\n", type == RTE_ETH_EVENT_INTR_LSC ? "LSC interrupt"
: "unknown event");
        rte eth link_get(port_id, &link);
        if (link.link_status) {
            printf("Port %d Link Up - speed %u Mbps - %s\n\n",
                    port_id, (unsigned)link.link_speed,
                    (link.link duplex == ETH LINK FULL DUPLEX) ?
                    ("full-duplex") : ("half-duplex"));
        } else
            printf("Port %d Link Down\n\n", port id);
}
```

This function is called when a link status interrupt is present for the right port. The port_id indicates which port the interrupt applies to. The type parameter identifies the interrupt event type, which currently can be RTE_ETH_EVENT_INTR_LSC only, but other types can be added in the future. The param parameter is the address of the parameter for the callback. This function should be implemented with care since it will be called in the interrupt host thread, which is different from the main thread of its caller.

The application registers the lsi_event_callback and a NULL parameter to the link status interrupt event on each port:

This registration can be done only after calling the rte_eth_dev_configure() function and before calling any other function. If lsc is initialized with 0, the callback is never called since no interrupt event would ever be present.

5.5 **RX Queue Initialization**

The application uses one lcore to poll one or several ports, depending on the -q option, which specifies the number of queues per lcore.



For example, if the user specifies -q 4, the application is able to poll four ports with one lcore. If there are 16 ports on the target (and if the portmask argument is -p ffff), the application will need four lcores to poll all the ports.

The list of queues that must be polled for a given lcore is stored in a private structure called struct lcore_queue_conf.

```
struct lcore_queue_conf {
    unsigned n_rx_queue;
    unsigned rx_queue_list[MAX_RX_QUEUE_PER_LCORE];
    unsigned tx_queue_id[LSI_MAX_PORTS];
    struct mbuf_table tx_mbufs[LSI_MAX_PORTS];
    } __rte_cache_aligned;
```

struct lcore_queue_conf lcore_queue_conf[RTE_MAX_LCORE];

The n_rx_queue and rx_queue_list[] fields are used in the main packet processing loop (see Section 5.7, "Receive, Process and Transmit Packets" on page 9).

The global configuration for the RX queues is stored in a static structure:

```
static const struct rte_eth_rxconf rx_conf = {
    .rx_thresh = {
        .pthresh = RX_PTHRESH,
        .hthresh = RX_HTHRESH,
        .wthresh = RX_WTHRESH,
    };
```

5.6 TX Queue Initialization

Each lcore should be able to transmit on any port. That is why there are as many TX queues as lcores. A limitation of this approach is that it is not possible to have more lcores than the maximum available TX queues for a given port.

For each lcore, the identifier of the queue to use to transmit on a given port is stored in struct lcore_queue_conf. This value is used when transmitting a packet on a port (see Section 5.7, "Receive, Process and Transmit Packets" on page 9).



The global configuration for RX gueues is stored in a static structure:

```
static const struct rte_eth_txconf tx_conf = {
    .tx_thresh = {
        .pthresh = TX_PTHRESH,
        .hthresh = TX_HTHRESH,
        .wthresh = TX_WTHRESH,
        .
        .tx_free_thresh = RTE_TEST_TX_DESC_DEFAULT + 1, /* disable feature */
};
```

5.7 Receive, Process and Transmit Packets

In the lsi_main_loop() function, the main task is to read ingress packets from the RX queues. This is done using the following code:

Packets are read in a burst of size MAX_PKT_BURST. The rte_eth_rx_burst() function writes the mbuf pointers in a local table and returns the number of available mbufs in the table.

Then, each mbuf in the table is processed by the lsi_simple forward() function. The processing is very simple: processes the TX port from the RX port and then replaces the source and destination MAC addresses.

Note: In the following code, the two lines for calculating the output port require some explanation. If portId is even, the first line does nothing (as portid & 1 will be 0), and the second line adds 1. If portId is odd, the first line subtracts one and the second line does nothing. Therefore, 0 goes to 1, and 1 to 0, 2 goes to 3 and 3 to 2, and so on.

```
static void
lsi_simple_forward(struct rte_mbuf *m, unsigned portid)
{
    struct ether_hdr *eth;
    void *tmp;
    unsigned dst_port = lsi_dst_ports[portid];
    eth = rte_pktmbuf_mtod(m, struct ether_hdr *);
    /* 00:09:c0:00:00:xx */
    tmp = &eth->d_addr.addr_bytes[0];
    *((uint64_t *)tmp) = 0x000000c00900 + (dst_port << 24);
    /* src addr */
    ether_addr_copy(&lsi_ports_eth_addr[dst_port], &eth->s_addr);
    lsi_send_packet(m, dst_port);
}
```



Then, the packet is sent using the lsi_send_packet (m, dst_port) function. For this test application, the processing is exactly the same for all packets arriving on the same RX port. Therefore, it would have been possible to call the lsi_send_burst() function directly from the main loop to send all the received packets on the same TX port using the burst-oriented send function, which is more efficient.

However, in real-life applications (such as, L3 routing), packet N is not necessarily forwarded on the same port as packet N-1. The application is implemented to illustrate that so the same approach can be reused in a more complex application.

The lsi_send_packet() function stores the packet in a per-lcore and per-txport table. If the table is full, the whole packets table is transmitted using the lsi send burst() function:

```
/* Send the packet on an output interface */
static int
lsi send packet(struct rte mbuf *m, uint8 t port)
{
    unsigned lcore_id, len;
    struct lcore_queue_conf *qconf;
    lcore id = rte lcore id();
    qconf = &lcore_queue_conf[lcore_id];
    len = qconf->tx_mbufs[port].len;
    qconf->tx mbufs[port].m table[len] = m;
    len++;
    /* enough pkts to be sent */
    if (unlikely(len == MAX PKT BURST)) {
        lsi_send_burst(qconf, MAX_PKT_BURST, port);
        len = 0;
    }
    qconf->tx_mbufs[port].len = len;
    return 0;
}
```

To ensure that no packets remain in the tables, each lcore does a draining of the TX queue in its main loop. This technique introduces some latency when there are not many packets to send. However, it improves performance:

```
cur_tsc = rte_rdtsc();
/*
* TX burst queue drain
*/
diff_tsc = cur_tsc - prev_tsc;
if (unlikely(diff_tsc > BURST_TX_DRAIN)) {
    /* this could be optimized (use queueid instead of
     * portid), but it is not called so often */
    for (portid = 0; portid < LSI_MAX_PORTS; portid++) {</pre>
        if (qconf->tx mbufs[portid].len == 0)
            continue;
        lsi send burst(&lcore queue conf[lcore id],
                       qconf->tx_mbufs[portid].len,
                       portid);
        qconf->tx_mbufs[portid].len = 0;
    }
    prev_tsc = cur_tsc;
}
```

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